

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Yamazaki, Arai, and Teramoto      Art Unit : 2775  
 Serial No.: 09/504,235                              Examiner :  
 Filed        : February 15, 2000                      Unknown  
 Title        : METHOD OF MANUFACTURING A SEMICONDUCTOR DEVICE

Commissioner for Patents  
 Washington, D.C. 20231

#5

INFORMATION DISCLOSURE STATEMENT

Applicant submits the references listed on the attached form PTO-1449, copies of which are enclosed.

This statement is being filed within three months of the filing date of the application or before the receipt of a first Office action on the merits.

This IDS is intended to be in full compliance with the rules, but should the Examiner find any part of its required content to have been omitted, prompt notice to that effect is earnestly solicited, along with additional time under Rule 97(f), to enable Applicant to comply fully.

Consideration of the foregoing and enclosures plus the return of a copy of the enclosed Form PTO-1449 with the Examiner's initials in the left column per MPEP 609 are earnestly solicited along with an early action on the merits.

## CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, Washington, D.C. 20231.

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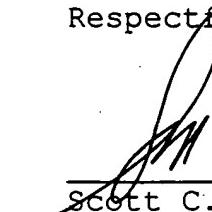
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Respectfully submitted,

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Substitute Form PTO-1449 (Modified)	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 07977-008004	Application No. 09/504,235
<b>Information Disclosure Statement by Applicant</b> (Use several sheets if necessary)		Applicant Shunpei Yamazaki, Yasuyuki Arai, and Satoshi Teramoto	
(37 CFR §1.98(b))		Filing Date February 15, 2000	Group Art Unit 2775

**U.S. Patent Documents**

Examiner Initial	Desig. ID	Patent Number	Issue Date	Patentee	Class	Subclass	Filing Date If Appropriate
	AA	5,147,826	09/15/92	Liu, et al.			
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						

**Foreign Patent Documents or Published Foreign Patent Applications**

Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation	
							Yes	No
	AL	WO 92/12453	07/23/92	PCT			X	
	AM							
	AN							
	AO							
	AP							

**Other Documents (include Author, Title, Date, and Place of Publication)**

Examiner Initial	Desig. ID	Document
	AQ	Gang Liu & S.J. Fonash, Selective area crystallization of amorphous silicon films by low-temperature rapid thermal annealing, Appl. Phys. Lett. 55, pp. 660-662, 1989
	AR	
	AS	
	AT	

Examiner Signature	Date Considered
EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	